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AUG 15 2001

2815
PTO/SB/21 (08-00)

Approved for use through 10/31/2002. OMB 0651-0031

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| TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i> | | Application Number | 09/583,087 |
| | | Filing Date | September 5, 2000 |
| | | First Named Inventor | Shunpei YAMAZAKI |
| | | Group Art Unit | 2815 |
| | | Examiner Name | J. Jackson, Jr. |
| Total Number of Pages in This Submission | | Attorney Docket Number | 0756-2203 |

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| Firm or Individual name | Eric J. Robinson, Reg. No. 38,285 Nixon Peabody LLP 8180 Greensboro Drive Suite 800 McLean, VA 22012 |
| Signature | |
| Date | August 15, 2001 |

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of)
Shunpei YAMAZAKI) Group Art Unit: 2815
Serial No. 09/583,087) Examiner: J. Jackson, Jr.
Filed: September 5, 2000)
For: ELECTRO-OPTICAL DEVICE AND)
METHOD FOR MANUFACTURING)
THE SAME)

Election
13/B
RECEIVED
TC 2815 AUG 20 2001
THROUGH
MAIL ROOM

AMENDMENT AND RESPONSE TO ELECTION REQUIREMENT

Honorable Commissioner for Patents

Washington, D.C. 20231

Sir:

In response to the Office Action of June 15, 2001, please amend the subject application as follows:

IN THE CLAIMS:

Cancel claims 42 without prejudice to applicants' right to file a divisional application with respect thereto.

Please amend claims 21-32, 41, 43-44, 48, 55, 59, 61, 62, and 63 as follows:

sub C1 21. (Amended) An active matrix display device having a pixel portion and a driver circuit portion, said driver circuit portion including at least one pair of complementary p-channel and n-channel thin film transistors, and said pixel portion including at least one thin film transistor, each of said transistors comprising:

B1 a crystalline semiconductor island on an insulating surface, said semiconductor island having source and drain regions and a channel region;

a gate insulating film adjacent to at least said channel region;

a gate electrode adjacent to said gate insulating film; and

a leveling film covering each of said thin film transistors in both of the pixel portion and a part of the driver circuit portion,